

#5

6477032

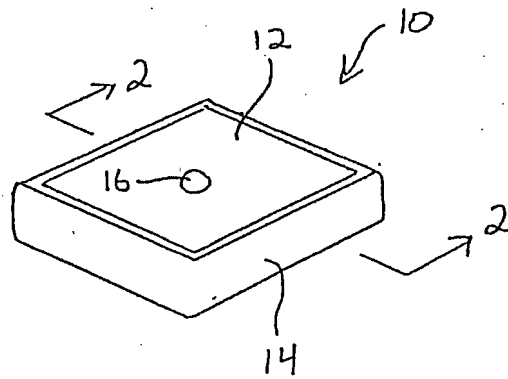


FIG. 1

8 Figs



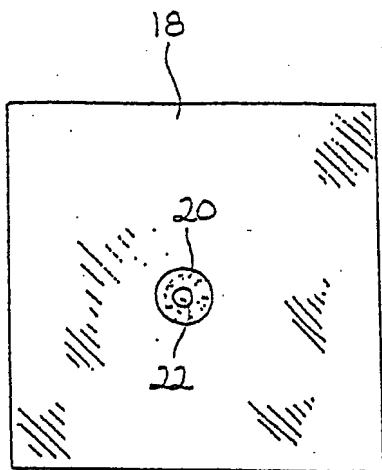


FIG. 3A

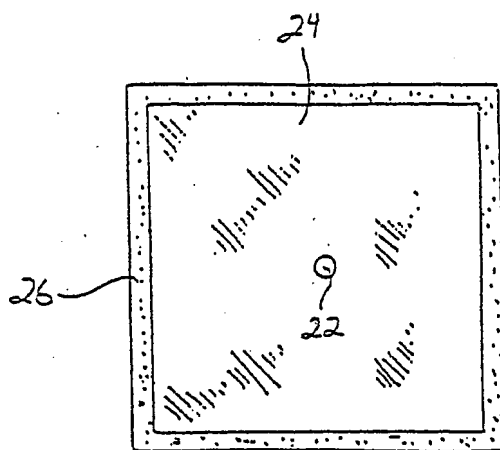


FIG. 3B

FIG. 3A

POSSIBLE 0505/200

VIA LAYOUT FOR CONTACTS FOR MICROPROCESSOR

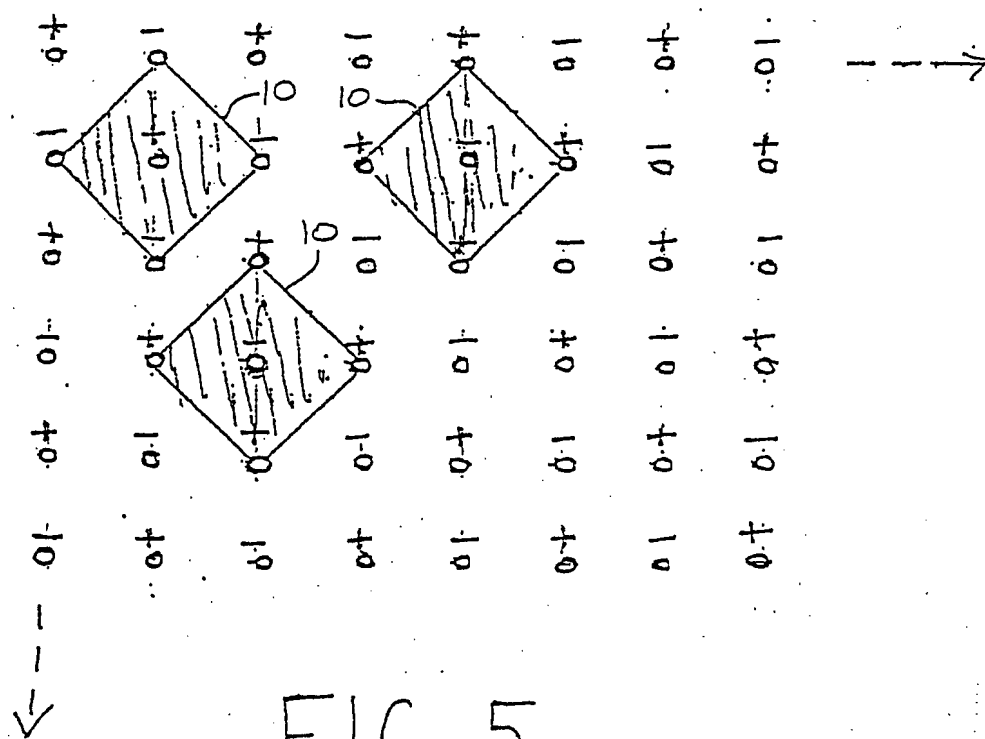


FIG. 5

FIGS. 7A & 7B

28

28

FIG. 7A

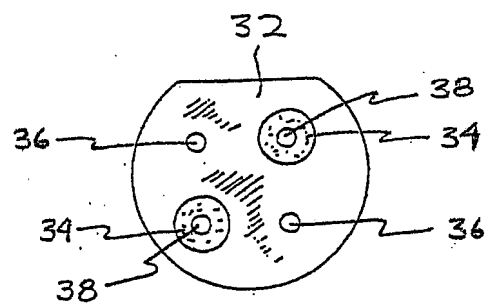


FIG. 7B